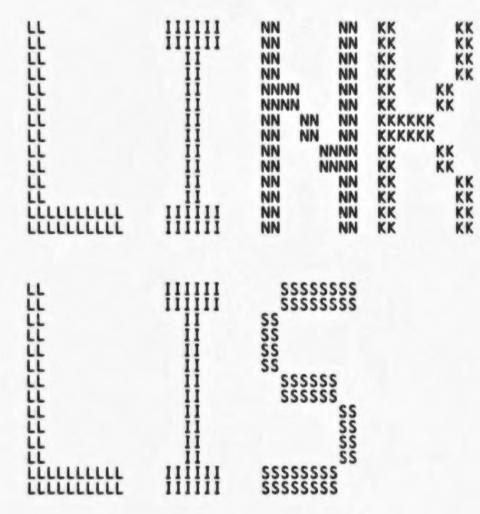
MMM	AAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAAA		RRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRRR	000000000 000000000 0000000000 000 000 000 000
MMM MMM	AAA AAA	2222222222	RRR RRR	000000000

\_\$

....



MAC VO4

0

Page

(1)

V04

.title mac\$link .ident 'V04-000'

G 3

link directive processor

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Facility:

VAX-11 Macro Assembler

Abstract:

This module contains the routines required to handle the .LINK assembler directive. The nature of this directive is to allow the user to specify linker options within the object module produced by the assembler.

Environment:

Native Mode, User Mode

Author:

Michael T. Rhodes,

Creation Date: April, 1983

Modified By:

MTR0036 Michael T. Rhodes 16-Aug-1 Add abbreviated qualifier name synonyms and adjust CASE V03-001 MTR0036 16-Aug-1983 table dispatch address to accomodate the LNK\$C\_SHR object record type.

16789012345678901 0000

0000 0000 0000

lnk\$c\_obj lnk\$c\_oli lnk\$c\_olb,

lnk\_qualifiers

Page (3)

MAC

Sym

SYM

D 100 .sbttl .link -- process the .link directive
D 101 :++
D 102 :

Functional Description:

This routine is called to process the .LINK directive. The valid syntax for this directive is as follows:

LINK "filespec"[/qualifier[=module or (module list)]],...

The filespec within the delimiters is scanned (built into a .ASCID string), then we scan looking for a .LINK directive qualifier. If none are present, the default linker option record type (regardless of the extension specified in the filespec) is OBJECT (which includes symbol tables).

# Implicit Inputs:

mac\$ab\_tmpbuf adr The address of the assembler's temporary buffer used to accumulate the delimited file specification.

mac\$ab\_tmpsym adr The address of the assembler's temporary buffer used to accumulate the qualifier name(s) and module name(s) specified in an include list.

mac\$gq\_lnkopt adr The address of the head of the linker options records queue.

# Implicit Outputs:

Linker option record(s) are placed into an ordered queue (mac\$gq\_lnkopt) where the order is preserved F1FO. This is done to remain compatible with the LINKER's normal processing of option records (as if they were speicified in a normal linker options file). The list is subsequently written to the object file during pass 2, following the object module header information.

### Special Case(s):

Special processing is performed when the /INCLUDE qualifier is specified. The object module names contained in the include list are constructed as .ASCIC strings following the filespec. The record is terminated by a module name string with a zero length.

If both the /LIBRARY and /INCLUDE qualifiers are specified for the same library filespec, then the linker option record type is defaulted to LNK\$C\_OLI and a special flag bit is set to indicate that the library may be searched (LNK\$V\_LIBSRCH).

#### Side Effects:

Two possible side effects can occur. The first is a recoverable syntax error where a diagnotic is issued to inform the user of the problem and the assembly of the current input file continues. The second is an insufficient virtual memory error in which a diagnotic is issued to the user and the assembly of the current input file is aborted.

MACSLINK VO4-000 MAC Pse

PSE.

SAB MAC MAC MAC

Pha Ini Com Pas Sym Pas

Sym Pse Cro Ass

The 586 The 688 37

-\$2 TOT 960

The

\$mac\_err dirsynx

100\$

#lnk\_l\_states, sp #^m<785

g^mac\$errorpt

Smac\_err untermarg

brb

addl

popr

IMP

; A directive syntax error has been

: Issue error message and return.

: A terminator has; the file name.

Restore the stack. Restore current token.

; encountered, issue error and return.

A terminator has not been seen for

805:

90\$:

1005:

05

5E 10 0100 8F

00000000 GF

11

CO BA 17

Tab

```
get_file_name accumulate file name
     .sbttl get_file_name
                                                          accumulate file name
                     Functional Description:
                             This routine scans the input record for a delimitted file name.
                      Inputs:
                             4(AP)
                                                 The address of a descriptor which points to a buffer to store the file name which we will scan.
                                       adr
                      Outputs:
                             4(AP)
                                                 The descriptor has been updated to reflect the size
                                                 of the file name which has been accumulated.
                      Routine Value:
                                       A file name has been scanned.
                             True
                             False
                                       No file name has been found (end of line or unterminated arg).
                      Side Effects:
                             If there is no file name available (eg. we hit the end of the line) the length field of the descriptor will be zero upon exit or if the argument is unterminated, the length will be non-zero but the routine
                             value will be false.
                                       get_file_name ^m<r2,r6>
                                                                       Save registers upon entry.
                             .entry
                                                                       Preserve R1.
                             pushl
                                       4(ap), r1
                                                                       Get descriptor address.
                             movl
                             clrl
                             movl
                                       dsc$a_pointer(r1), r2
      00D2
                             clrl
                                                                       Assume the worst...
      00D4
                                                                       find the delimiter.
                                       q*mac$skipsp
      OODA
                                       r10,#cr
                                                                       Have we reached the end of the line?
                             cmpb
      OODD
                                       30$
                                                                       Yes, return FALSE to the caller.
                             beal
```

DD D0 D4 D0 D4 16 91 390 C8 51 52 0000 00000000 5A 20 5A 01 OD 56 6B OODF 00E2 OOES 00000000 GF 56 5A 0C 0D 5A 0A 82 5A 61 E9 105: 16 91 13 91 13 90 86 11 OOF O OOF 3 00F 5 OOF OOF OOF C 01 51 01 20\$: 30\$: 50 DO 8EDO **OOF F 6B** 

0106

r10, r6 movb bisl2 #flg\$m\_allchr,(r11) g^mac\$getchr jsb r10, r6 cmpb beal r10,#cr cmpb beal 30\$ r10,(r2)+movb incw (r1)10\$ brb movl popl bicl2 #flqSm\_allchr,(r11)

ret

Initialize length, class, and type fields. Get the buffer address. No. copy the delimiter and pass semi colons (to allow a version number).

Get the next character of the filename. Is it the delimiter (end of filename)? Yes, we're done here, return. No, is it the end of the line? Yes, upon return issue unterminated argume No, store the character. Keep track of file name length. Gather the rest of the file name.

Success... Restore R1. : Don't pass anymore semi-colons...

(5)

VAX/VMS Macro V04-00 [MACRO.SRC]LINK.MAR;1

```
process link directive qualifiers
                                                                .sbttl process_qual
                               Functional Description:
                                                                This routine processes the .LINK directive qualifiers.
                                                     Inputs:
                                                                4(AP)
                                                                             adr
                                                                                           Address of a linker record vector.
                                                     Outputs:
                                                                4(AP)
                                                                                           The Linker record information is set in the vector.
                                                                             adr
                                                     Routine Value:
                                                                             Qualifiers have been processed without a problem. There was a syntax error in either the qualifier name or in the item list associated with the qualifier.
                                                                True
                                                                False
                     01E0
9E
04
00
9E
                                                                                                                                      Save registers.
Get STACK LOCAL storage.
Initialize done bit.
Get base adr of link info vector.
Base address of file name list hea
                                                                             process_qual ^m<r5,r6,r7,r8>
                                                                .entry
  5E
          F8
                                                                movab
                                                                             -8(sp), sp
                                                                clrl
                                                                              (sp)
                                                                              4(ap), r7
                                                                movl
                                                                              lnk_q_inclst (r7), r8
                                                                movab
            5A
0D
5A
08
5A
06
00A9
                                                                                                                                       Did we stop on a comma?
Yes, we're done with this file spe
No, have we reached eol?
Yes, we're done, return.
                        91
13
91
13
91
13
13
13
13
                                                  105:
       20
                                                                              r10, #^A/,/
                                                                CMDD
                                                                             30$
r10, #cr
                                                                begl
       OD
                                                                cmpb
                                                                begl
       2F
                                                                                                                                       Is the current character slash? Yes, scan qualifier name.
                                                                cmpb
                                                                              40$
                                                                beal
                                                  20$:
                                                                                                                                       No, syntax error.
                                                                brw
                                                                brw
                                                                                                                                       Done, return success.
00000000 GF
00000000 GF
EB 50
0000000 GF
00000000 GF
00000000 GF
                                                  405:
                        16 16 9E 16 E 9 16
                                                                isb
                                                                             g^mac$getchr
                                                                                                                                       Yes, skip over it ...
                                                                             g^mac$symscnup
r0, 20$
lnk_qualifiers, r5
                                                                                                                                       Get the qualifier name.
                                                                jsb
                                                                                                                                       None found, error.
Use linker qualifier name table.
Look up linker option qualifier.
                                                                blbc
                                                                movab
                                                                             g^mac$src_list
r0, 20$
                                                                jsb
                                                                                                                                    : Not found, error.
: Position character pointer as need
                                                                blbc
                                           3601
3663
3663
3667
3667
3768
3771
                                                                             g^mac$skipsp
                                                                isb
                                                     Dispatch to appropriate processing routine.
                                                                        sym$l_val(r1), #0, #lnk$c_maxrectyp
60$-50$
70$-50$
80$-50$
90$-50$
70$-50$
                     000C*
002E*
003A*
0067*
002E*
  00
          05 A1
                                                                casel
                                                                                                                                      Ink$c_olb - /LIBRARY
Ink$c_shr - (unsupported)
Ink$c_oli - /INCLUDE=
Ink$c_obj - /SELECTIVE_SEARCH
Ink$c_sha - /SHAREABLE
Default, OBJ or STB
                                                  50$:
                                                                .word
                                                                .word
                                                                .word
                                                                .word
                                                                .word
                                                                brb
                               0161
```

M 3

link directive processor 16-SEP-1984 02:06:27 process\_qual process link directive qual 5-SEP-1984 01:48:43

0161 374 0161 375	/LIBRARY	Normal object library	
01 A7 95 0161 376 06 13 0164 378 02 01 A7 91 0166 379 65 12 016A 380 01 A7 00 90 016C 381 00 6E 00 E2 0170 382 08 6E 01 E1 0174 383 02 A7 02 88 0178 384 01 A7 02 90 017C 385 FF93 31 0180 386	63\$: movb bbss bbc	<pre>lnk\$b_lnktyp (r7) 63\$ lnk\$b_lnktyp (r7), #lnk\$c_oli 110\$ #lnk\$c_olb, lnk\$b_lnktyp (r7) #0, (sp), +1 #1, (sp), 65\$ #lnk\$m_libsrch, lnk\$w_flags (r7) #lnk\$c_oli, lnk\$b_lnktyp (r7) 10\$</pre>	Check for conflicting qualifiers. None specified. If /INCLUDE was specified, no conf but anything else will conflict. Ink\$c_olb - Normal object library. Indicate /LIBRARY has been specifi If /INCLUDE has been specified, the library should be searched and type precedence goes to LNK\$C_OLI. Get the next entity.
0183 388 0183 389	/SHAREABLE	Shareable Image	
01 A7 95 0183 391 49 12 0186 392 01 A7 04 90 0188 393 FF87 31 018C 394	65\$: brw  /SHAREABLE  70\$: tstb bneq movb brw	<pre>lnk\$b_lnktyp (r7) 110\$ #lnk\$c_sha, lnk\$b_lnktyp (r7) 10\$</pre>	Check for conflicting qualifiers. We have a conflict. Ink\$c sha - Shareable Image Get the next entity.
018F 395 018F 396 018F 397	/INCLUDE	Object Library with Inclu	ide list
3D 5A 91 01AA 407 22 12 01AD 408 000001D4'EF 00 FB 01AF 409 18 50 E9 01B6 410 FF5A 31 01B9 411	*/INCLUDE  **80\$: tstb beql cmpb bneq bneq bbss bbc bisb cmpb bneq calls blbc brw	<pre>82\$ lnk\$b_lnktyp (r7), #lnk\$c_olb 110\$ #lnk\$c_oli, lnk\$b_lnktyp (r7) #1, (sp), +1 #0, (sp), 83\$ #lnk\$m_libsrch, lnk\$w_flags (r7); r10, #*A/=/ 110\$ #0, get_incl_list r0, 110\$</pre>	Check for conflicting qualifiers. None specified. If /LIBRARY was specified, no conf but anything else will conflict. Ink\$c_oli - Object Library with an Indicate /INCLUDE has been specifi If /LIBRARY has been specified, the library should be searched. The next character should be an '= If not, its a syntax error. Get the module name(s) in the incl Issue syntax error. Cet the next entity.
01BC 413 01BC 414 01BC 415	/SELECTIVE_SE	ARCH Selective search of OLB o	r STB
01 A7 95 01BC 416 10 12 01BF 417 02 A7 01 88 01C1 418	bisb	<pre>lnk\$5_lnktyp (r7) 110\$ #lnk\$m_selser, lnk\$w_flags (r7) #lnk\$c_obj, lnk\$b_lnktyp (r7) 10\$</pre>	Check for conflicting qualifiers. We have a conflict. lnk\$v_selser - Selective search lnk\$c_obj - Object Module Get the next entity.
01cc 423	All done sele	ect status and return.	
01 A7 03 90 01C5 419	100\$: movl brb 110\$: clrl 120\$: ret	120s r0	Success. Now return. Error. Restore registers and return.

get\_incl\_list ^m<>
-4(sp), sp 0000 0104 .entry Get the module names to include. 5E 01D6 Get LOCAL STORAGE. FC AE movab 01DA cirl : Initialize local storage. (90) 01DC 16 16 91 13 OIDC 00000000 GF 457 10\$: g^mac\$getchr jsb Get the next character. g mac skipsp 00000000 GF isb Skip spaces, tubs, etc.. r10, #cr 5A UD cmpb End of line? 01EB 460 80\$ : Yes, syntax error. beal 461 205: 28 91 13 91 13 r10, #^A/(/ cmpb Do we have a list of names? 30\$ Yes, remove open paren and indicat beal 20 r10, #^A/,/ Check for module name delimiter. cmpb 40\$ Remove the comma, and validate str beal Do we have a close paren?
Yes, end of the list?
End of line?
We're done, select return status.
Reset comma seen flag. 29 466 r10, #^A/)/ cmpb 467 beal 50\$ 91 OD r10, #cr cmpb begl 60\$ bbsc E4 16 E9 F8 #0, (sp), .+1 00000000 Get the module name. No file name, error. isb g^mac\$symscnup ro. 80\$ blbc 0000024B'EF 00 #0, insert\_module calls Insert this module name into the l Get the next module (if any). 06 brb 30\$: bbss 26 6E 10\$ Check for syntax error -- 2 or mor (sp), 80\$ BF Indicate a list and continue the m brb E1 E2 405: bbc (sp), Comma seperated list not allowed o #0, (sp), 80\$ 00 bbss To many commas? Remove comma, step to next module brb 16 6E 01 000000000 GF 58 E1 16 505: bbc #1, (sp), 80\$ Should we have a close paren? g^mac\$getchr r8 (r8) 80\$ 70\$ jsb Yes, skip it for correct grammatic Have we got at least one module? No, and we don't accept null lists D1 cmpl begl Everything looks ok, return succes brb

MAC!

29191AADAFD04CDE613991FF02AA

MACSLINK V04-000				lini get_	direc incl_l	tive ist G	proces et the	ssor module(s)	C	4 the	ι	16-SEP-1984 5-SEP-1984	02:06:27 01:48:43	VAX [MA	(/VMS Macro V04-00 NCRO.SRCJLINK.MAR;1	Page	10 (7)
	05	6E 50	01 01 09	E0 D0 11	0238 0238 0230 0230 023F	488 489 490 491 492	60\$: 70\$: 80\$:	bbs movl brb	#1, #1, 90\$	·		80\$		:	Should we have a clos Parse successful return success.	e paren?	(pre
	04	68 A8	50 58 58	04 00 04	0241 0243 0246 024A 024B	494 495 496 497	80\$: 90\$:	clri movi movi ret	r0 r8, r8,	(r)	8) r8)				Parse failed. Fake an empty queue reset the list head. Return	•	

> FF FF

011

FF.

FF FF

FF:

FF

FF:

OO FF

FF

27 27 01

00 27

Insufficient Virtual Memory, report error and abort this assembly.

028C 548 ins\_vir\_mem:
09000900'GF 00 FB 028C 549 calls #0, g^mac\$err\_nomem\_0 ; Report error.
00000000'GF 17 0293 550 jmp g^mac\$last\_chance ; Abort this assembly.

01

MAC VO4

00

FF

FF

FF

27

00

FF

FF

FF FF

FF

FF.

00

FF.

FF

FF.

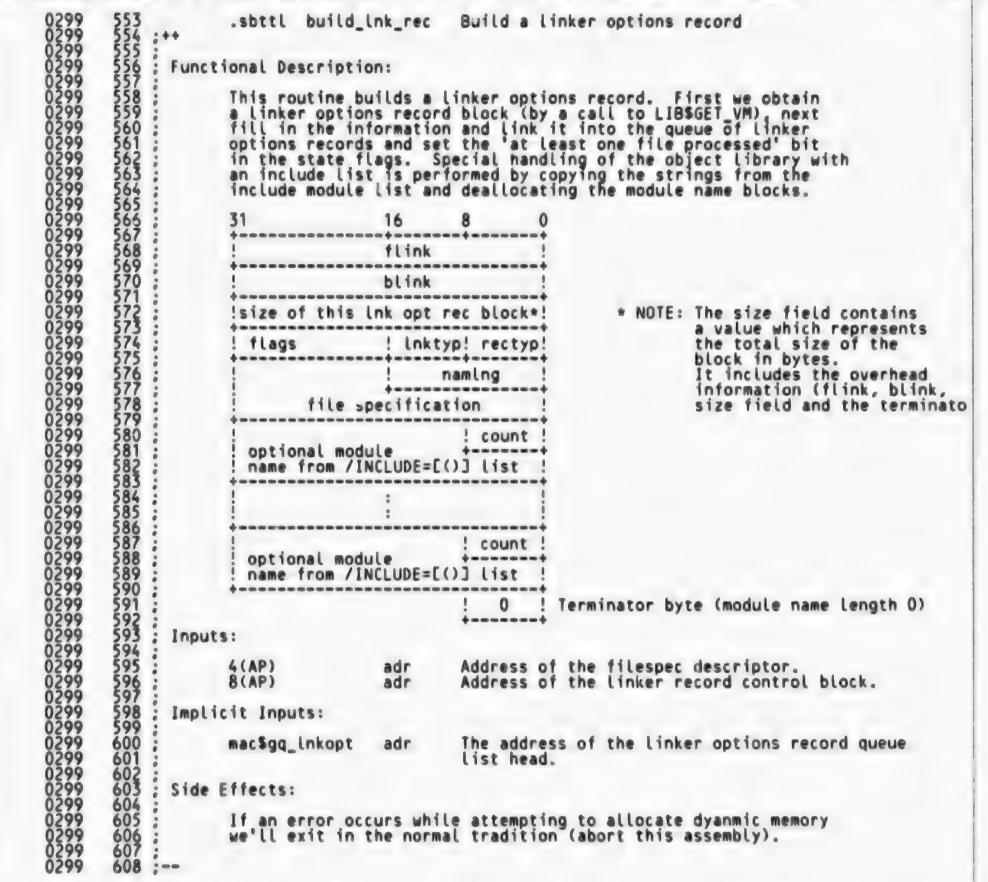
01

FF

00 FF

08

FF



021 FF

MAC VO4

> 021 FF

> > FF

FF D8

FF

FF.

03

FF

FF D8

00000000 GF

68

00

04 B6 10 A7

00

Mark include list terminator.

#0, lnk\_l\_states(r7),.+1; Set flag indicating at least 1 file proces

Get the address of the linker options queu Insert this record into the linker options

MAC VO4

```
build_lnk_rec ^m<r2,r3,r4,r5,r6,r7,r8>
4(ap), r2
Get the address
8(ap), r7
Get link con
                                                                                      .entry
                                                                611
612
613
                                                                                                                                                   Get the address of the filespec.
Get link control block address.
Allocate STACK LOCAL storage.
                            04
                                           00024fC109fB000000088
                                                                                      movL
                                                                                      movl
                         5E
                                                                                      sub12
                                                                                                    #8, sp
                                                                                                                                                    Initialize the return address scalar.
Push the address of the return address sca
                                                                cirl
                                                                                      pushab
                                                                                                     4(sp)
                                                                                                    dscsw_length (r2),4(sp); G
4(sp),lnk_l_bytes(r7).4(sp)
#lnk_blk_siz, 4(sp); I
                                                                                     movzwl
addl3
addl2
                                                                                                                                                    Get the length.
           04 AE
                         0000
                   A7
04
04 AE
              00
                             04
                                                                                                                                                    ); Compute the #bytes req for this record Include the fixed area size in the count.
                                                                                                                                                   Address of the number of byte req.
Allocate memory for this linker options re
Insufficient Virtual Memory error.
Get the beginning address of the linker op
Preserve the block address for later use.
Advance pointer to first data field.
                             04
                                                                                      pushab
                                                                                                    4(SD)
                                                                                                    #2, g^lib$get_vm
r0, ins_vir_mem
4(sp), r3
r3, r8
#8, r3
         00000000 GF
                                                                                      calls
                             04
                                                                                      blbc
                    53
                                  AE 53 08
                                                                                      movl
                58
53
83
83
83
0000'02
                                                                                      movl
                                                                                      addl2
                                  6E
67
62
                                                                                                    (sp), (r3)+
(r7), (r3)+
                                                                                                                                                    Copy the number of bytes in this block.
                                                                                      MOVL
                                                                                                    movl
                                                                                      MOVW
       63
                                                                                      movc3
                                                                       105:
                                                                                                    alnk_q_inclst(r7), r6
20$
                    56
                                                                                                                                                    Remove the next module name.
                                                                                      remaue
                                           100
9F
9A
80
28
CO
9F
FB
11
                                                                                                                                                    Is the queue empty?
                                                                                      DVS
                                                                                                                                                   No, get the address of the module name block to release and pass it by reference. Get the string length and include the count byte in the string size.
                                                                                                    r6, 4(sp)
                                                                                      movl
                             04
                                                                                                    4(sp)
                                                                                      pushab
                                                                                                    8(r6), 4(sp)
                             08
                                                                                      movzbl
                    04 AE
                                  01
                                                                                      addb2
                                                                                                    #1, 4(sp)
                                                                                                    4(sp), 8(r6), (r3)
#8, 4(sp)
                             04
                                                                                                                                                    Copy the module size/name to the record. The linkage is included in the mnb size. Pass the block size by reference too.
                                  AE
08
                    A6
                                                                                      movc3
addl2
                    04
                                  AE
02
                             04
                                                                                      pushab
                                                                                                    4(SD)
                                                                                                    #2, g^lib$free_vm
10$
         00000000 GF
                                                                                      calls
                                                                                                                                                    Release the module name block.
                                                                640
                                  04
                                                                                                                                                    Get the next module.
                                                                                      brb
                                                                641
                                                                642
643
644
645
                                           94
9E
0E
E2
                                                                       205:
```

(r3)

g^mac\$gq\_lnkopt, r6 (r8), a4(r6)

clrb

bbss

ret

646

movab

insque

Functional Description:

This routine removes the linker options records from the queue MACSGQ\_LNKOPT and writes them to the object module (following the GSD).

.sbttl mac\$wrt\_inkopt Write the linker options records to object

Implicit Inputs:

Contains the address of the object code buffer. The address of the linker option record queue. R10 mac\$gq\_lnkopt adr

Side Effects:

All linker option record(s) have been written to the object file and the currect object record buffer type will be set to OBJ\$C\_TIR upon exit.

661 6663 6663 6667 6668 6670 6771 6775 6777 0040 .entry macSwrt\_lnkopt ^m<r6> 00000000 FF 105: amac\$gq\_lnkopt, r6 remque 1D7 C38 D00 SF D00 SF bvs decl #12, 8(r6), r1 r1, 12(r6), (r10) subl3 A6 OC. movc3 r3, r10 movl bsbw mac\$wrtobi 04 AE r6, 4(sp) movl 04 pushab 4(sp) 8(r6), 4(sp) 04 movl pushab 4(sp) #2 g^lib\$free\_vm 00000000 GF calls CD brb 684 685 686 687 90 205: movb #obj\$c\_tir, (r10) ret 035E 688 ; of MODULE mac\$link . end

660

: ++

Save register(s). Allocate STACK LOCAL storage. Get a linker option record. Is the queue empty? No, set the buffer pointer to origin. Compute the size of this record. Copy the record to the object code buffer. Update the object code pointer. Write the object record. Release dynamic memory...
Pass the block's address by reference.
The linker option record's block size is also passed by reference. Release this block. Continue until the queue is empty.

All done, correct the object record type to assume TIR.

Psect synopsis

PSECT name	Allocation			PSECT		Attribu										
. ABS . BLANK . SABSS MACSRW_DATA MACSRO_DATA MACSRO_CODE_P1	00000000 00000000 00000013 00000008 0000007D 0000035E	00000	0.) 0.) 19.) 8.) 125.) 862.)	00 ( 01 ( 02 ( 03 ( 04 (	0.)	NOPIC NOPIC NOPIC NOPIC NOPIC NOPIC	USR USR USR USR USR USR	CON CON CON CON CON	ABS REL ABS REL REL	LCL LCL LCL GBL GBL	NOSHR NOSHR NOSHR NOSHR NOSHR NOSHR	NOEXE EXE NOEXE NOEXE EXE	NORD RD RD RD RD RD	NOWRT WRT WRT WRT NOWRT	NOVEC NOVEC NOVEC NOVEC NOVEC	BYTE BYTE BYTE BYTE LONG LONG

1 4

16-SEP-1984 02:06:27 VAX/VMS Macro V04-00 5-SEP-1984 01:48:43 [MACRO.SRCJLINK.MAR;1

# Performance indicators

Phase	Page faults	CPU Time	Elapsed Time
Initialization	32	00:00:00.06	00:00:00.46
Command processing	108	00:00:00.38	00:00:03.67
Pass 1	340	00:00:07.38	00:00:31.50
Symbol table sort	0	00:00:00.82	00:00:02.10
Pass 2	137	00:00:01.81	00:00:03.82
Symbol table output	22	00:00:00.12	00:00:00.12
Psect synopsis output	-3	00:00:00.03	00:00:00.03
Cross-reference output	ő	00:00:00.00	00:00:00.00
Assembler run totals	644	00:00:10.60	00:00:41.70

The working set limit was 1650 pages. 58640 bytes (115 pages) of virtual memory were used to buffer the intermediate code. There were 50 pages of symbol table space allocated to hold 787 non-local and 45 local symbols. 688 source lines were read in Pass 1, producing 36 object records in Pass 2. 37 pages of virtual memory were used to define 36 macros.

Macro library statistics !

# Macro library name

MACSLINK

Psect synopsis

Macros defined

-\$255\$DUA28:[MACRO.OBJ]MACRO.MLB;1 -\$255\$DUA28:[SYSLIB]STARLET.MLB;2 TOTALS (all libraries) 10

960 GETS were required to define 10 macros.

There were no errors, warnings or information messages.

MACRO/LIS=LIS\$:LINK/OBJ=OBJ\$:LINK MSRC\$:LINK/UPDATE=(ENH\$:LINK)+LIB\$:MACRO/LIB

Sym

MAC

0226 AH-BT13A-SE

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